


Accessories


EMC-PG01L / EMC-PG02L



Set by Pr.10-00 ~ 10-02

Terminals		Description
PG1	VP	Output voltage for power: +5V/+12V ± 5% (use FSW3 to switch +5V/+12V) Max. output current: 200mA
	DCM	Common for power and signal
	A1, /A1, B1, /B1, Z1, /Z1	Encoder input signal (Line Driver) Open collector input: +5 V / +24 V (Note1) 1-phase or 2-phase input Max. input frequency: EMC-PG01L: 300KHz; EMC-PG02L: 30KHz
PG2	A2, /A2, B2, /B2	Pulse input signal (Line Driver or Open Collector) Open collector input: +5V/+24 V (Note1) 1-phase or 2-phase input Max. input frequency: EMC-PG01L: 300KHz; EMC-PG02L: 30KHz
PG OUT	AO, /AO, BO, /BO, ZO, /ZO, SG	PG card output signals. Division frequency function: 1 ~ 255 times Max. output voltage for Line driver: 5V _{DC} Max. output current: 50 mA Max. output frequency: EMC-PG01L: 300KHz; EMC-PG02L: 30KHz SG: The GND of PG card is the same as the host controller or PLC, so a common output signal is attained.


EMC-PG01O / EMC-PG02O



Set by Pr.10-00 ~ 10-02

Terminals		Description
PG1	VP	Output voltage for power: +5V/+12V ± 5% (use FSW3 to switch +5V/+12V) Max. output current: 200mA
	DCM	Common for power and signal
	A1, /A1, B1, /B1, Z1, /Z1	Encoder input signal (Line Driver or Open Collector) Open collector input: +5V/+24 V (Note1) 1-phase or 2-phase input Max. input frequency: EMC-PG01O: 300KHz; EMC-PG02O: 30KHz
PG2	A2, /A2, B2, /B2	Pulse input signal (Line Driver or Open Collector) Open collector input: +5V/+24 V (Note1) 1-phase or 2-phase input Max. input frequency: EMC-PG01O: 300KHz; EMC-PG02O: 30KHz
PG OUT	V+, /V-	Needs external power source for PG OUT circuit. Input voltage of power: +12V ~ +24V
	V-	Negative power supply input
	A/O, B/O, Z/O	PG card output signals. Division frequency function: 1 ~ 255 times Add a pull-up resistor to the open collector output signals to avoid signal interferences. [Three pull-up resistors are included in the package (1.8KΩ/1W)] Max. Output current: 20mA Max output frequency: EMC-PG01O: 300KHz; EMC-PG02O: 30KHz

EMC-PG01R



Set by Pr.10-00 ~ 10-02

Terminals		Description
PG1	R1- R2	Resolver output power 7V _{rms} , 10kHz
	S1, S2, S3, S4	Resolver input signal 3.5 ± 0.175V _{rms} , 10kHz
PG2	A2, /A2, B2, /B2	Pulse input signal (Line Driver or Open Collector) Open collector input: +5V/+24 V (Note1) 1-phase or 2-phase input; Max. input frequency: 300 KHz
PG OUT	AO, /AO, BO, /BO, ZO, /ZO, SG	PG card output signals. Division frequency function: 1 ~ 255 times Max. output voltage for Line driver: 5V _{DC} Max. output current: 50 mA Max. output frequency: 300 KHz SG: The GND of PG card is the same as the host controller or PLC, so a common output signal is attained.